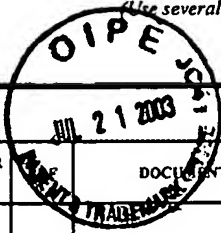


INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)



Docket Number (Optional)

BUR920030031 US1

Application Number

10/2004/116

Applicant(s)

Anderson et al.

Filing Date

4/26/03

Group Art Unit

2823

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						YES	NO
JAG	EP0721221A2	7/1996	EP	—	—		

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

JAG	Doo, V.Y., "Method of Making Submicron Channel MOSFET," IBM Technical Disclosure Bulletin, Vol. 24, No. 7A, December 1981, pp. 3407-3408.

EXAMINER

James A. Dore

DATE CONSIDERED

May 19, 2005

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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Title of Invention	METHOD OF FORMING FREESTANDING SEMICONDUCTOR LAYER																																												
<p>Application Number: 10/604,116 Confirmation Number: First Named Applicant: Brent Anderson Attorney Docket Number: BUR920030031US1 Search string: (5723370 or 6492212 or 20020181523),pn.</p> <p>US Patent Documents</p> <p>Note: Applicant is not required to submit a paper copy of cited US Patent Documents</p> <table border="1"><thead><tr><th>init</th><th>Cite.No.</th><th>Patent No.</th><th>Date</th><th>Patentee</th><th>Kind</th><th>Class</th><th>Subclass</th></tr></thead><tbody><tr><td><input checked="" type="checkbox"/></td><td>1</td><td>5723370</td><td>1998-03-03</td><td>Ning et al</td><td></td><td></td><td></td></tr><tr><td><input checked="" type="checkbox"/></td><td>2</td><td>6492212</td><td>2002-12-10</td><td>leong, et al</td><td></td><td></td><td></td></tr></tbody></table> <p>US Published Applications</p> <p>Note: Applicant is not required to submit a paper copy of cited US Published Applications</p> <table border="1"><thead><tr><th>init</th><th>Cite.No.</th><th>Pub. No.</th><th>Date</th><th>Applicant</th><th>Kind</th><th>Class</th><th>Subclass</th></tr></thead><tbody><tr><td><input checked="" type="checkbox"/></td><td>1</td><td>20020181523</td><td>2002-12-05</td><td>Pinneo, et al</td><td></td><td></td><td></td></tr></tbody></table> <p>Signature</p> <table border="1"><thead><tr><th>Examiner Name</th><th>Date</th></tr></thead><tbody><tr><td><i>Joanni A. Garza</i></td><td><i>May 19, 2005</i></td></tr></tbody></table>		init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass	<input checked="" type="checkbox"/>	1	5723370	1998-03-03	Ning et al				<input checked="" type="checkbox"/>	2	6492212	2002-12-10	leong, et al				init	Cite.No.	Pub. No.	Date	Applicant	Kind	Class	Subclass	<input checked="" type="checkbox"/>	1	20020181523	2002-12-05	Pinneo, et al				Examiner Name	Date	<i>Joanni A. Garza</i>	<i>May 19, 2005</i>
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